## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): A.Q. Khan et al.

Case:

4-14-28

Serial No.:

To Be Assigned

Filing Date:

November 26, 2003

Group:

To Be Assigned

Examiner:

To Be Assigned

Title:

Processor with Scheduler Architecture Supporting

Multiple Distinct Scheduling Algorithms

## INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. A copy of each listed item is enclosed.

## U.S. Patent Documents

- 1. U.S. Application Serial No. 10/085,219, filed in the name of D.B. Kramer et al. on February 28, 2002 and entitled "Processor with Dynamic Table-Based Scheduling Using Linked Transmission Elements for Handling Transmission Request Collisions."
- 2. U.S. Application Serial No. 10/085,223, filed in the name of D.B. Kramer et al. on February 28, 2002 and entitled "Processor with Dynamic Table-Based Scheduling Using Multi-Entry Table Locations for Handling Transmission Request Collisions."
- 3. U.S. Application Serial No. 10/085,222, filed in the name of D.B. Kramer et al. on February 28, 2002 and entitled "Processor with Table-Based Scheduling Using Software-Controlled Interval Computation."

4. U.S. Application Serial No. 10/085,771, filed in the name of D.B. Kramer et al. on February 28, 2002 and entitled "Processor with Software-Controlled Programmable Service Levels."

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

Date: November 26, 2003

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## FORM PTO-1449 (MODIFIED)

LIST OF PUBLICATIONS FOR **APPLICANT'S INFORMATION** DISCLOSURE STATEMENT

Applicant(s): A.Q. Khan et al. Case: 4-14-28

Serial No.: TBA

November 26, 2003 Filing Date:

TBA Group:

		U	S. PATENT DOCUMENTS			
EXAMINER					FILING DAT	TE
INITIAL	DOCUMENT NO.	DATE	NAME C	LASS/SUBCLASS	IF APPROPI	RIATE
1.	10/085,219	02/28/02	D.B. Kramer et al., "Processor with Dynamic Table-Based Scheduling Using Linked Transmission Elements for Handling Transmission Request Collisions."			
2.	10/085,223	02/28/02	D.B. Kramer et al., "Processor with Dynamic Table-Based Scheduling Using Multi-Entry Table Locations for Handling Transmission Request Collisions."			
3.	10/085,222	02/28/02	D.B. Kramer et al., "Processor with Table-Based Scheduling Using Software-Controlled Interval Computation."			
4.	10/085,771	02/28/02	D.B. Kramer et al., "Processor with Software-Controlled Programmable Service Levels."			
		FOR	EIGN PATENT DOCUMENTS			
EXAMINER					TRANSLATI	ION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES N	NO
_			OTHER DOCUMENTS			
DV A MAINIDD			OTHER DOCUMENTS		·	
EXAMINER INITIAL	REF NO. AUT	HOD TITLE DATE D	ERTINENT PAGES, ETC.			
MITIAL	KEFNU. AUI	nok, HILE, DAIE, P	ERTINENT FAGES, ETC.			

Examiner Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.